



Sample Program Guideline

DMA transfer (between UART and internal RAM) (V850/SA1™, V850/SB1™, V850/SB2™)

This document describes the sample program that performs application-independent general-purpose control in the V850/SA1, V850/SB1, and V850/SB2.

This document consists of the following two sections.

- Outline
- Flow chart

*Sample codes can be downloaded from the Microcomputer web page.

<Related document>

V850/SA1 Application Note (Document number: U13851E)

Caution

This software is for reference only; NEC does not guarantee the operation. Therefore, evaluate it sufficiently beforehand with the set to be actually used.

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Outline

Two channels are used for DMA transfers. One channel is transferred from the on-chip RAM to the USRT transmit register as the transfer trigger source for INST1 and the other channel is transferred from the UART receive register to the on-chip RAM as the transfer trigger source for INTSR1. The UART is operated with a loopback setting that enables the transmit data to be received without modification.

Flow chart

