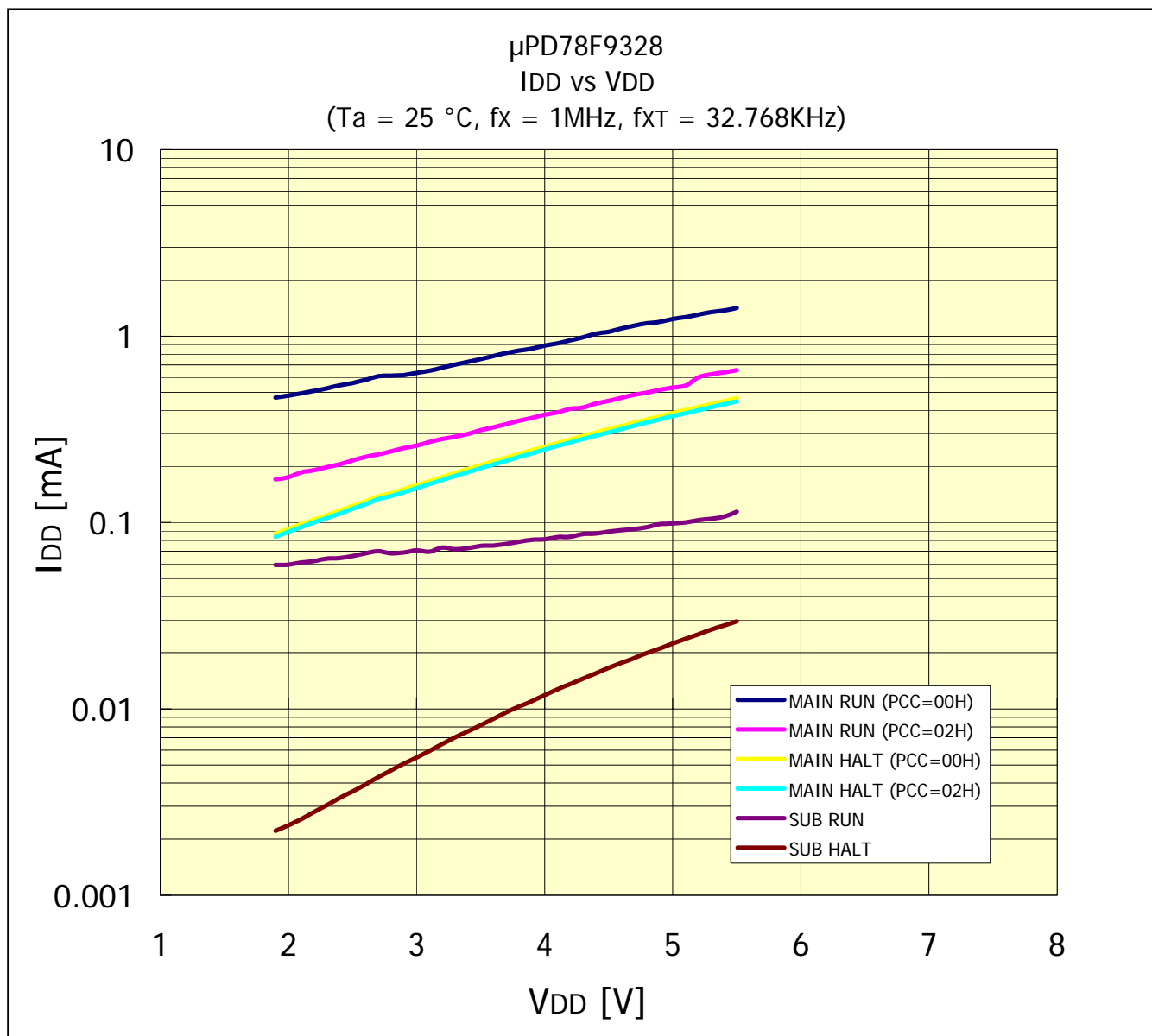


# IDD vs VDD (25°C / 1MHz)

Prepared on Jun. 13th, 2003

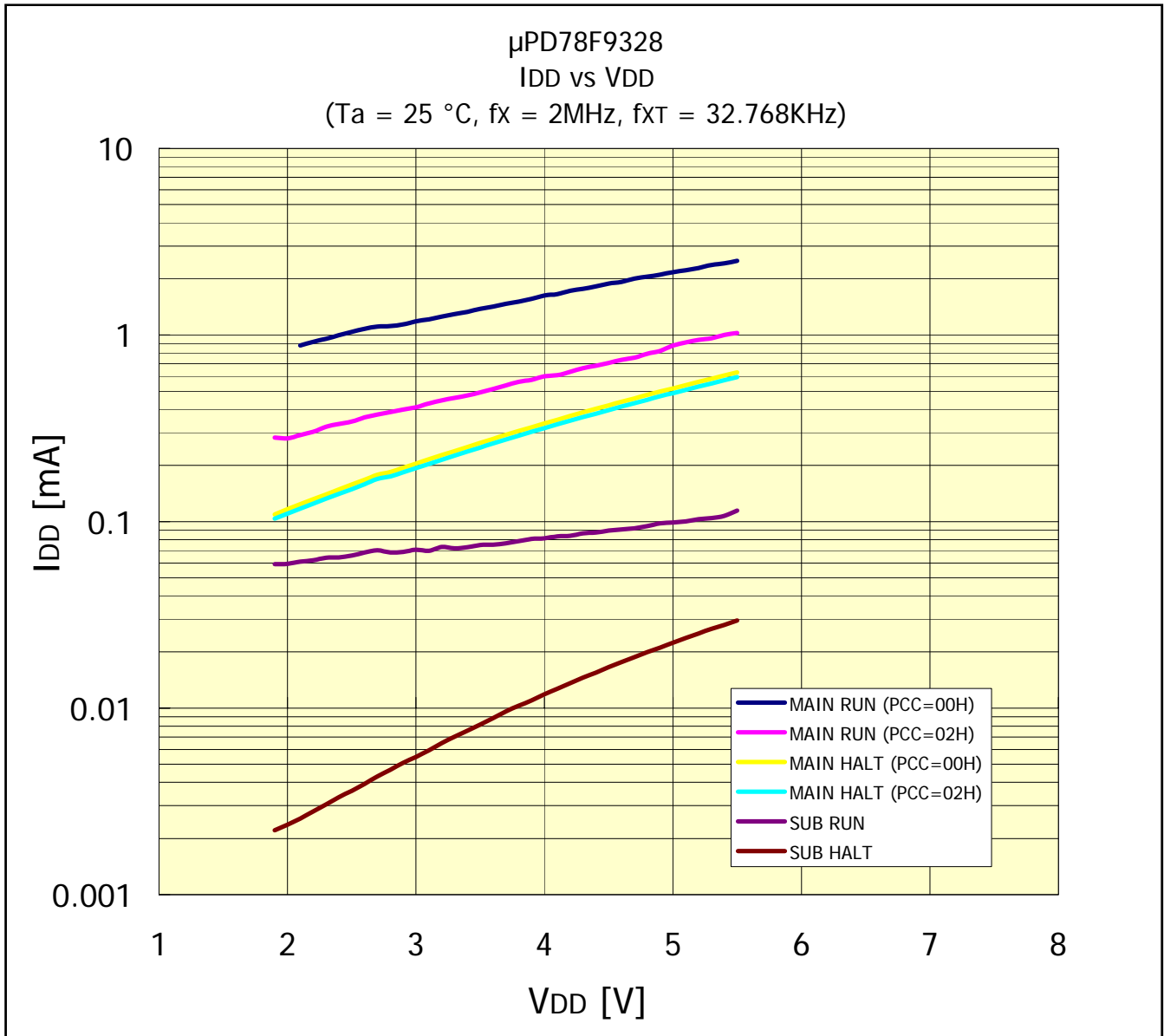


	Main System Clock	Sub System Clock
MAIN RUN	Oscillating	Oscillating
MAIN HALT	Oscillating	Oscillating
SUB RUN	Stopped	Oscillating
SUB HALT	Stopped	Oscillating

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

# IDD vs VDD (25°C / 2MHz)

Prepared on Jun. 13th, 2003

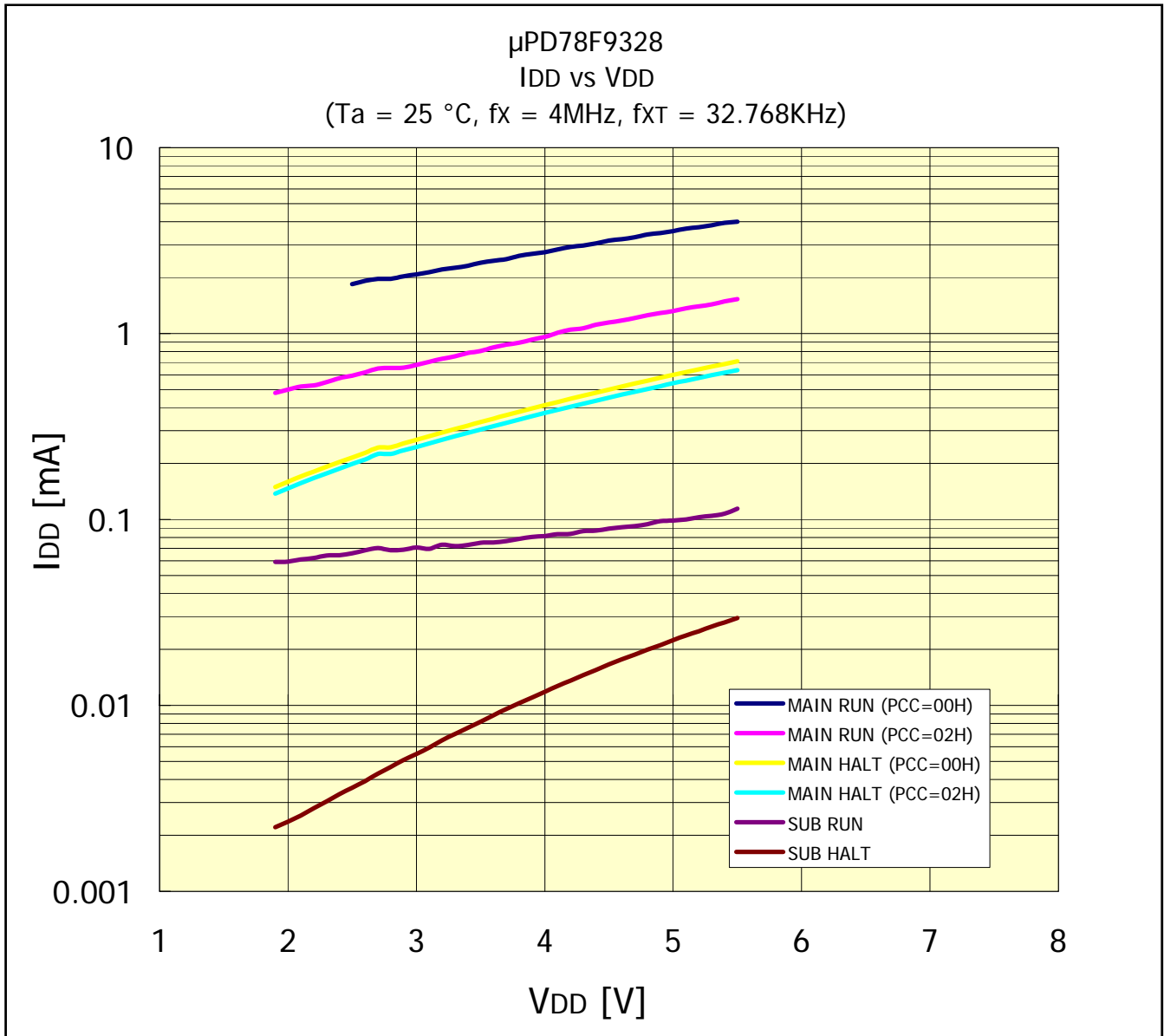


	Main System Clock	Sub System Clock
MAIN RUN	Oscillating	Oscillating
MAIN HALT	Oscillating	Oscillating
SUB RUN	Stopped	Oscillating
SUB HALT	Stopped	Oscillating

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

# IDD vs VDD (25°C / 4MHz)

Prepared on Jun. 13th, 2003

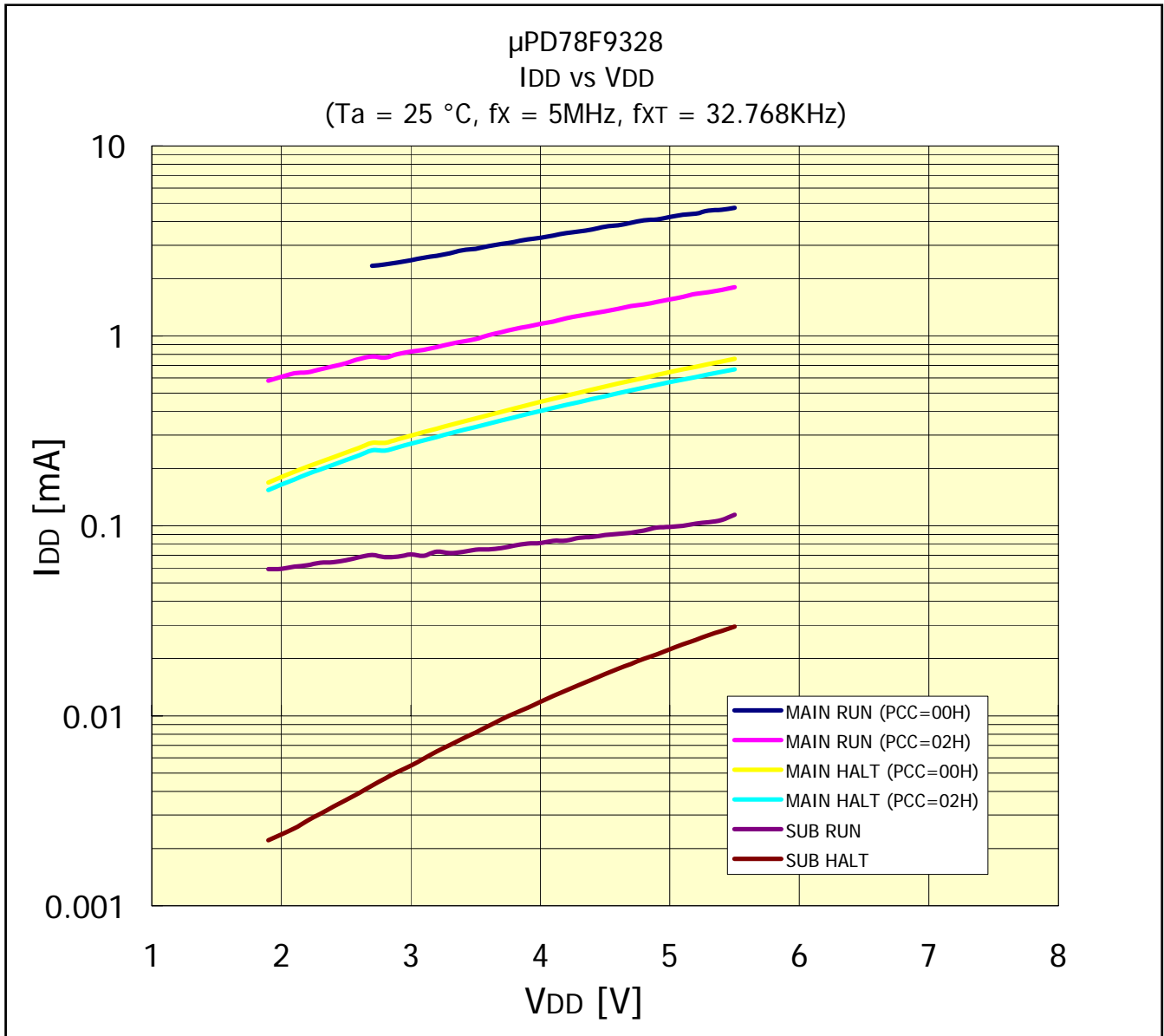


	Main System Clock	Sub System Clock
MAIN RUN	Oscillating	Oscillating
MAIN HALT	Oscillating	Oscillating
SUB RUN	Stopped	Oscillating
SUB HALT	Stopped	Oscillating

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

# IDD vs VDD (25°C / 5MHz)

Prepared on Jun. 13th, 2003



	Main System Clock	Sub System Clock
MAIN RUN	Oscillating	Oscillating
MAIN HALT	Oscillating	Oscillating
SUB RUN	Stopped	Oscillating
SUB HALT	Stopped	Oscillating

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.